

PATENT

FO466



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Date: 8-12-02

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2826
TECHNICAL UNIT 2800
AUG 23 2002
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Applicants: Michael Fliesler, et al.

Serial No: 10/050,394

Filing Date: January 16, 2002

Examiner: Thomas L. Dickey

Art Unit: 2826

Title: IMPROVED DIODE FABRICATION FOR ESD/EOS PROTECTION

Assistant Commissioner for Patents
U.S. Patent and Trademark Office
Washington, D.C. 20231

REPLY TO RESTRICTION REQUIREMENT MAILED AUGUST 5, 2002

This Reply is in response to the Restriction Requirement mailed on August 5, 2002 in connection with the above-identified patent application.

The Examiner requires restriction to one of the following two groups of claims:

(I) claims 21-24 or (II) 1-20.


Applicants' representative hereby elects with traverse group II (claims 1-20 drawn to a device) for further prosecution on the merits.

Should there be any questions regarding this paper, the Examiner is invited to contact applicants' undersigned representative at the telephone number listed below.

In the event any fees are due in connection with submission of this document, the Commissioner is authorized to charge such fees to Deposit Account No. 50-1063.

Respectfully submitted,

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